

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	145	(microcontroller same master same slave) and emulat\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/28 13:48
L2	582	(target adj (device microcontroller)) and fpga	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/28 13:48
L3	24	L2 and lock\$step	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/28 13:48
L5	479	(precomput\$4 predict\$4) same ((I/O input output) and jump)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/28 13:50
L6	26	(precomput\$4 predict\$4) same ((I/O input output) and jump) and microcontroller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/28 13:50
L7	346	(fpga same (access visibility) same (dut microcontroller target))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/28 13:50
L8	65	L7 and emulat\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/02/28 13:50